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22850  
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 ALEXANDRIA, VA 22314

CONFIRMATION NO. 3827  
**CORRECTED FILING RECEIPT**  
  
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Date Mailed: 08/25/2004

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**Applicant(s)**

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**Assignment For Published Patent Application**

Renesas Technology Corp., Tokyo, JAPAN;

**Domestic Priority data as claimed by applicant**

This application is a DIV of 10/201,921 07/25/2002 PAT 6,756,692

**Foreign Applications**

JAPAN 2001-243292 08/10/2001

**If Required, Foreign Filing License Granted: 06/08/2004**

**Projected Publication Date: 09/16/2004**

**Non-Publication Request: No**

**Early Publication Request: No**

**Title**

Semiconductor storage device having high soft-error immunity

**Preliminary Class**

257

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